

**Notice of Allowability**

Application No.

10/614,918

Examiner

James A. Dudek

Applicant(s)

KAWATA, HIDENORI

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**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the election filed 10/28/04.
2. ☒ The allowed claim(s) is/are 1-14.
3. ☒ The drawings filed on 09 July 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some\* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- \* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

**REASONS FOR ALLOWANCE**

The following is an examiner's statement of reasons for allowance:

Regarding claim 1 and its associated dependent claims and claim 13, the prior art of record teaches an electro-optical apparatus, comprising: a substrate; a pixel electrode provided above the substrate; a thin film transistor to perform switching control of the pixel electrode; a data line provided on an upper layer side of the thin film transistor, and which supplies an image signal to the pixel electrode via the thin film transistor; a storage capacitor, including a pixel-potential-side capacitor electrode, provided on the upper layer side of the thin film transistor, and which is electrically connected between the thin film transistor and the pixel electrode; a dielectric film; a capacitor line including a fixed-potential-side capacitor electrode provided on the upper layer side of the pixel-potential-side capacitor electrode, facing the pixel-potential-side capacitor electrode with the dielectric film disposed therebetween, and which is provided with a notch portion corresponding to a connection region to connect the pixel-potential-side capacitor electrode and the pixel electrode; and the pixel-potential-side capacitor electrode including a first conductive transparent film, the capacitor line including a first conductive light-shielding film, the data line including a second conductive light-shielding film, and the pixel electrode including a second conductive transparent film. The prior art of record does not teach nor suggest in combination with the limitations supra a multilayer junction-layer transit-connected between the pixel-potential-side capacitor electrode and the pixel electrode, which has a laminated structure including a first layer including the second light-shielding film and a conductive second layer having a chemical stability against the second transparent film higher than a chemical stability of the second light-shielding film, laminated on the upper layer side of the first layer, and which is planar covering the notch portion in plan view and is further provided in the connection region.

Regarding claim 10 and its associated dependent claims and claim 14, the prior art of record teaches an electro-optical apparatus, comprising: a substrate; a pixel electrode provided above the substrate; a thin film transistor to perform switching control of the pixel electrode; a

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data line provided on an upper layer side of the thin film transistor, and which supplies an image signal to the pixel electrode via the thin film transistor; a storage capacitor, including a pixel-potential-side capacitor electrode, provided on the upper layer side of the thin film transistor, and which is electrically connected between the thin film transistor and the pixel electrode; a dielectric film; a capacitor line including a fixed-potential-side capacitor electrode provided on the upper layer side of the pixel-potential-side capacitor electrode, facing the pixel-potential-side capacitor electrode with the dielectric film therebetween, and which is provided with a notch portion corresponding to a connection region to connect the pixel-potential-side capacitor electrode and the pixel electrode; and the pixel-potential-side capacitor electrode including a first conductive transparent film, the capacitor line including a first conductive light-shielding film, the data line including a second conductive light-shielding film, the pixel electrode including a second conductive transparent film. The prior art of record does not teach nor suggest, in combination with the limitations supra a monolayer junction-layer transit-connected between the pixel-potential-side capacitor electrode and the pixel electrode, which includes the second light-shielding film, and which is planar covering the notch portion in plan view and is further provided in the connection region.

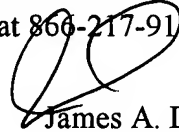
Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James A. Dudek whose telephone number is 571-272-2290. The examiner can normally be reached on 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H. Kim can be reached on 571-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



James A. Dudek  
Primary Examiner  
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